IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.:	10/044,217	Filing Date:	November 19, 2001
Confirmation No.:	9153		
First Named Inventor:	Robert M. Zeidman		
Title:	System and Method For Connecting A Logic Circuit Simulation To A Network		
Examiner:	Luu, Cuong V.	Art Unit:	2128
Attorney Docket No.:	M-8637-1P US		

San Jose, California
January 31, 2007

Via EFS-Web
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT

Dear Sir:

In response to the Office Action of January 16, 2007, Applicant amends the above-referenced patent application as follows and requests the Examiner to reconsider Claims 17-18 and 57-63, in light of the remarks below: